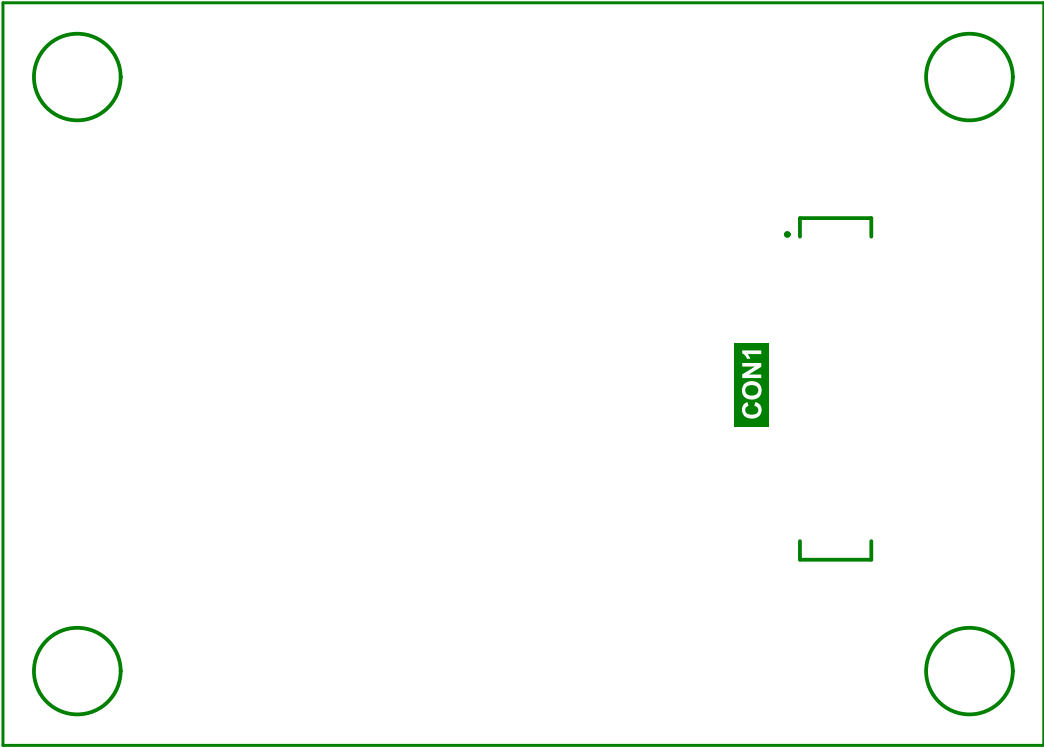


... RXD2  
... TXD2  
... Reser ved\_1  
... Reser ved\_2  
... Reser ved\_3  
... Reser ved\_4  
... Reser ved\_5  
... Reser ved\_6  
... FUPSTS\_OUT  
... /EMG\_RI  
... ANTINF\_0  
... ANTINF\_1  
... MOS\_IND  
... Reser ved\_7  
... Reser ved\_8  
... Reser ved\_9  
... Reser ved\_10  
... Reser ved\_11  
... Reser ved\_12  
... Reser ved\_13  
... Reser ved\_14  
... Reser ved\_15  
... UART\_INIT  
... MODE1  
... MODE2

TXD  
RXD  
DTR  
CTS  
RTS  
DCD  
DSR  
RI  
RESET\_CHK  
AREA\_IND  
PS\_HOLD  
Reser ved\_17  
eSIM\_CLK  
eSIM\_DATA  
eSIM\_RST  
eSIM\_VCC  
RESET\_N  
PWR\_ON  
Reser ved\_16  
UIM2\_VPP  
UIM2\_CLK  
UIM2\_DATA  
UIM2\_RESET\_N  
UIM2



TP1  
Reser ved\_17  
ON<->OFF

SW3  
GND  
USB  
S1

P1  
1 2 16 15  
VBUS D- D+ GND  
RXD CTS DCD RI DSR RTS +3.65\_VBATT DTR +3.65\_VBATT TXD

P2  
1 2 16 15  
PWR\_ON PS\_HOLD RESET\_N TXD2 RXD2  
GND

P4  
GND AD GND DA  
P5  
VDD3.0 SBWTK SBWTDIO GND

ON : Auto  
OFF : Manual  
S3  
1 2 10  
GND

RESET  
SW1 SW2

POWER  
LED55  
DC\_POWER  
+3.65\_VBATT  
TP5

8  
J2 HP OUT  
J3 LINE OUT  
J1 MIC/LINE IN  
LED53

RT1  
1 2  
D.MIC LINE  
ECM MIC  
S5  
1 2  
ON

Volume  
P3  
1 2 9 10  
Audio\_IN\_P Audio\_IN\_N GND Audio\_OUT\_N Audio\_OUT\_P

CN8  
UART1  
1 2  
OFF:USB/ON:UART1

CN7  
UART2  
1 5  
VDD3.0  
1 : ON  
2 : OFF=3.65V

CN3  
DATA  
1 10  
GND

POWER MODE  
1 2  
ON

ON<->OFF  
POWER SW4  
+3.65\_VBATT  
TP5

RESET  
SW1 SW2

TP2  
GND  
TP3  
VDD3.0  
1 : ON  
2 : OFF=3.65V  
TP4  
VDD\_Audio

DC IN  
CN6  
USB POWER2  
LED54

CN5  
UART2  
1 5  
GND

CN4  
USB/UART1  
1 5  
6 9

CN2  
UART1  
1 5  
6 9

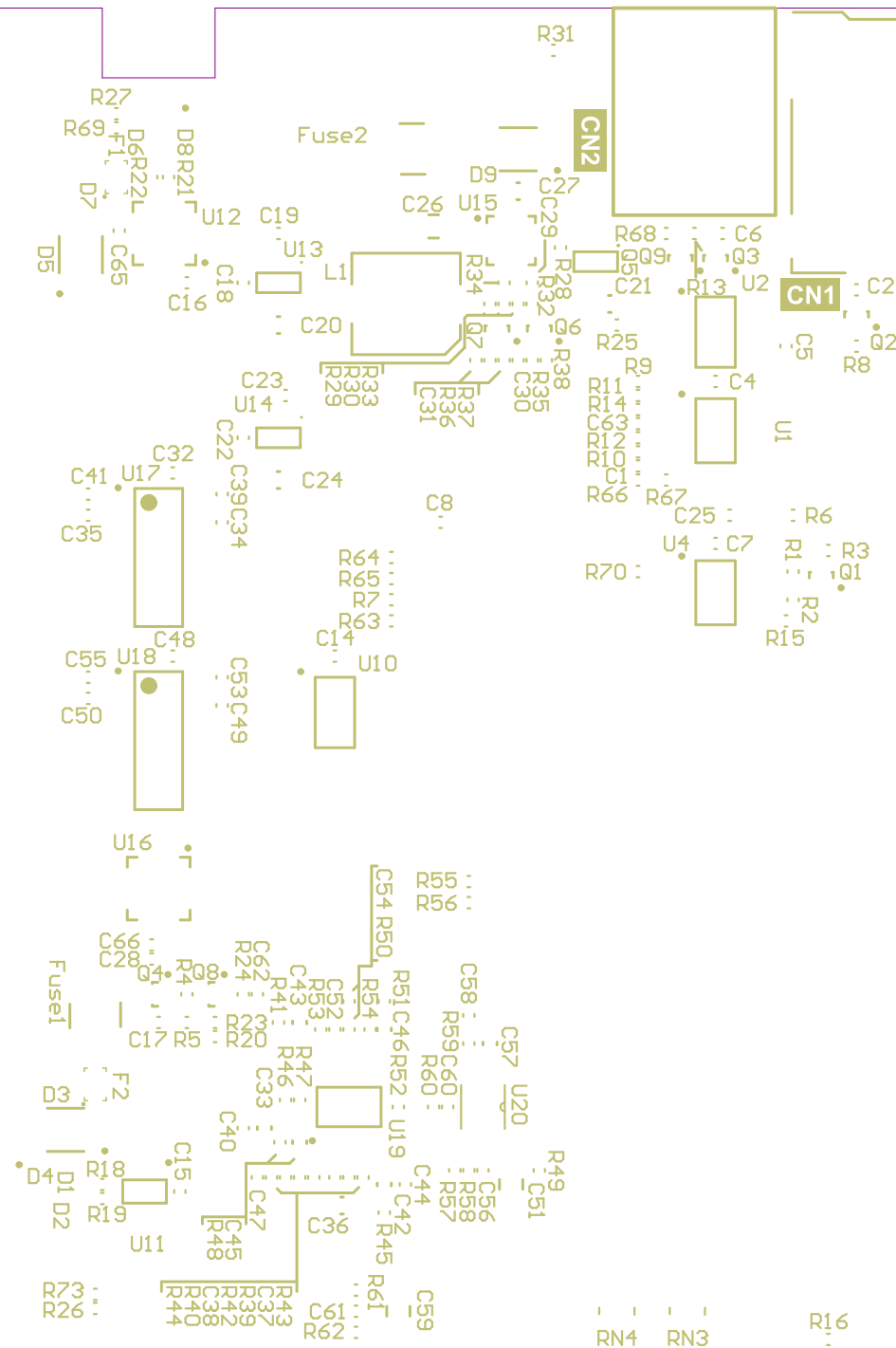
CN1  
UART2  
1 5  
6 9

CN1  
UART2  
1 5  
6 9

CN1  
UART2  
1 5  
6 9

CN1  
UART2  
1 5  
6 9





PCB KYMSDK-MAIN-TYPE001 Ver.1.30  
Wireless Design Co.,LTD.  
Made in JAPAN.

