

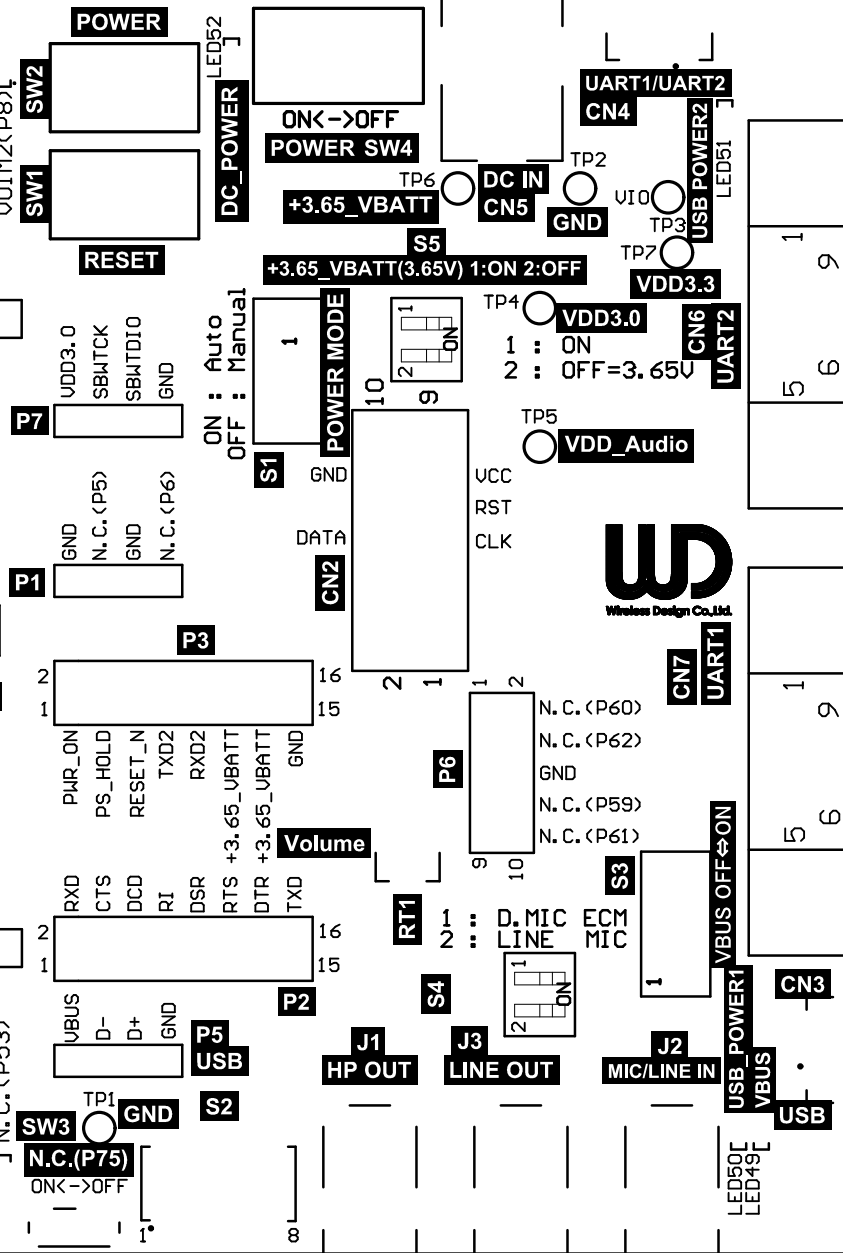
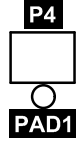
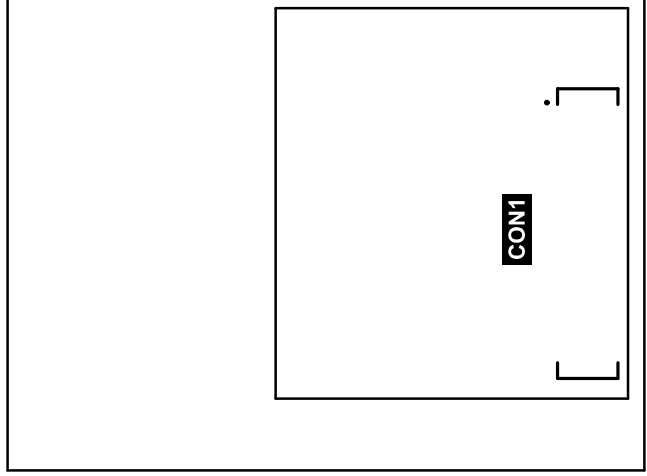


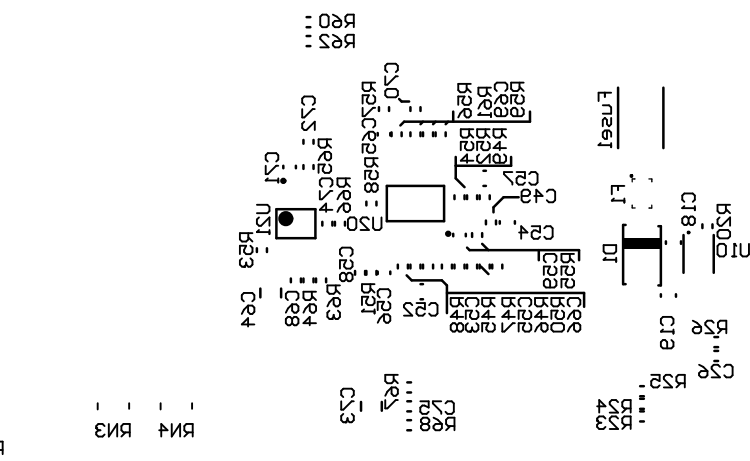
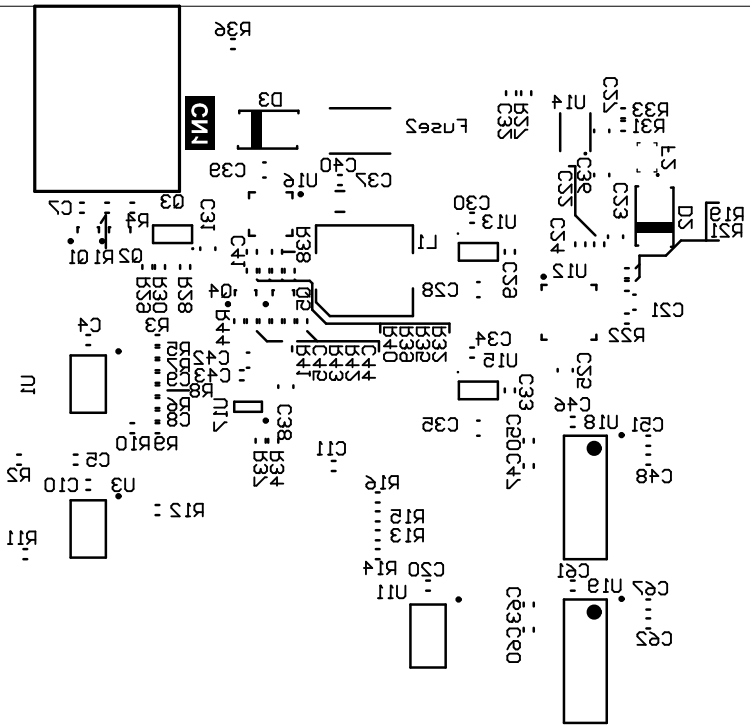
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Wireless Design Co., LTD.
K2DKR-TYPE001 Ver.1.01

